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GPGPU: general purpose computation on graphics hardware

David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn

August 2004 ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04

Publisher: ACM Press

Full text available: pdf(63.03 MB) Additional Information: full citation, abstract, citings

The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex and pixel processing units that support vector operations up to full IEEE floating point precision. High level languages have emerged for graphics hardware, making this computational power accessible. Architecturally, GPUs are highly parallel s ...

2 Real-time shading

Marc Olano, Kurt Akeley, John C. Hart, Wolfgang Heidrich, Michael McCool, Jason L. Mitchell, Randi Rost

August 2004 ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04

Publisher: ACM Press

Additional Information: full citation, abstract Full text available: pdf(7.39 MB)

Real-time procedural shading was once seen as a distant dream. When the first version of this course was offered four years ago, real-time shading was possible, but only with oneof-a-kind hardware or by combining the effects of tens to hundreds of rendering passes. Today, almost every new computer comes with graphics hardware capable of interactively executing shaders of thousands to tens of thousands of instructions. This course has been redesigned to address today's real-time shading capabili ...

3 A taxonomy of Data Grids for distributed data sharing, management, and processing

Srikumar Venuqopal, Rajkumar Buyya, Kotagiri Ramamohanarao June 2006 ACM Computing Surveys (CSUR), Volume 38 Issue 1

Publisher: ACM Press

Full text available: pdf(1.70 MB) Additional Information: full citation, abstract, references, index terms

Data Grids have been adopted as the next generation platform by many scientific communities that need to share, access, transport, process, and manage large data collections distributed worldwide. They combine high-end computing technologies with

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high-performance networking and wide-area storage management techniques. In this article, we discuss the key concepts behind Data Grids and compare them with other data sharing and distribution paradigms such as content delivery networks, peer-to-peer n ...

Keywords: Grid computing, data-intensive applications, replica management, virtual organizations

4 Link and channel measurement: A simple mechanism for capturing and replaying



wireless channels

Glenn Judd, Peter Steenkiste

August 2005 Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05

Publisher: ACM Press

Full text available: pdf(6.06 MB)

Additional Information: full citation, abstract, references, index terms

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An important challenge in physical emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we examine the possibility of using on-card signal strength measurements to capture wireless channel traces. A key advantage of this approach is the simplicity and ubiquity with which these measurements can be obtained since virtually all wireless devices provide the req ...

Keywords: channel capture, emulation, wireless

⁵ Security and eliability: Live updating operating systems using virtualization



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Haibo Chen, Rong Chen, Fengzhe Zhang, Binyu Zang, Pen-Chung Yew
June 2006 Proceedings of the second international conference on Virtual execution
environments VEE '06

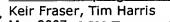
Publisher: ACM Press

Full text available: pdf(136.71 KB) Additional Information: full citation, abstract, references, index terms

Many critical IT infrastructures require non-disruptive operations. However, the operating systems thereon are far from perfect that patches and upgrades are frequently applied, in order to close vulnerabilities, add new features and enhance performance. To mitigate the loss of availability, such operating systems need to provide features such as live update through which patches and upgrades can be applied without having to stop and reboot the operating system. Unfortunately, most current live ...

Keywords: availability, live update, operating system, virtualization

6 Concurrent programming without locks



May 2007 ACM Transactions on Computer Systems (TOCS), Volume 25 Issue 2

Publisher: ACM Press

Full text available: pdf(1.58 MB) Additional Information: full citation, abstract, references, index terms

Mutual exclusion locks remain the *de facto* mechanism for concurrency control on shared-memory data structures. However, their apparent simplicity is deceptive: It is hard to design scalable locking strategies because locks can harbor problems such as priority inversion, deadlock, and convoying. Furthermore, scalable lock-based systems are not readily composable when building compound operations. In looking for solutions to these problems, interest has developed in *nonblocking* sy ...

Keywords: Concurrency, lock-free systems, transactional memory

7 The Direct3D 10 system

David Blythe

July 2006 ACM Transactions on Graphics (TOG), ACM SIGGRAPH 2006 Papers SIGGRAPH '06, Volume 25 Issue 3

Publisher: ACM Press

Full text available: pdf(377.38 KB) Additional Information: full citation, abstract, references, citings, index terms

We present a system architecture for the 4th generation of PC-class programmable graphics processing units (GPUs). The new pipeline features significant additions and changes to the prior generation pipeline including a new programmable stage capable of generating additional primitives and streaming primitive data to memory, an expanded, common feature set for all of the programmable stages, generalizations to vertex and image memory resources, and new storage formats. We also describ ...

Keywords: graphics systems, programmable graphics hardware, programmable shading

8 A principled approach to operating system construction in Haskell

Thomas Hallgren, Mark P. Jones, Rebekah Leslie, Andrew Tolmach September 2005 **ACM SIGPLAN Notices**, **Proceedings of the tenth ACM SIGPLAN**

international conference on Functional programming ICFP '05, Volume 40 Issue 9

Publisher: ACM Press

Full text available: pdf(154.82 KB) Additional Information: full citation, abstract, references, index terms

We describe a monadic interface to low-level hardware features that is a suitable basis for building operating systems in Haskell. The interface includes primitives for controlling memory management hardware, user-mode process execution, and low-level device I/O. The interface enforces memory safety in nearly all circumstances. Its behavior is specified in part by formal assertions written in a programming logic called P-Logic. The interface has been implemented on bare IA32 hardware using the G ...

Keywords: Haskell, hardware interface, monads, operating systems, programming logic, verification

⁹ An implementation and analysis of the virtual interface architecture

Philip Buonadonna, Andrew Geweke, David Culler

November 1998 Proceedings of the 1998 ACM/IEEE conference on Supercomputing (CDROM) Supercomputing '98

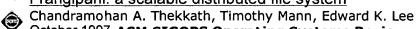
Publisher: IEEE Computer Society

Full text available: html(60.53 KB) Additional Information: full citation, abstract, references, citings

Rapid developments in networking technology and a rise in clustered computing have driven research studies in high performance communication architectures. In an effort to standardize the work in this area, industry leaders have developed the Virtual Interface Architecture (VIA) specification. This architecture seeks to provide an operating system-independent infrastructure for high-performance user-level networking in a generic environment. This paper evaluates the inherent costs and performanc ...

Keywords: cluster, interconnect, network, system-area, user-level, virtual interface architecture

10 Frangipani: a scalable distributed file system



October 1997 ACM SIGOPS Operating Systems Review, Proceedings of the sixteenth ACM symposium on Operating systems principles SOSP '97, Volume 31 Issue

Publisher: ACM Press

Full text available: pdf(2.20 MB) Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

11 DISE: a programmable macro engine for customizing applications

Marc L. Corliss, E. Christopher Lewis, Amir Roth

May 2003 ACM SIGARCH Computer Architecture News, Proceedings of the 30th annual international symposium on Computer architecture ISCA '03, Volume

Publisher: ACM Press

Full text available: 📆 pdf(335.30 KB) Additional Information: full citation, abstract, references, citings

Dynamic Instruction Stream Editing (DISE) is a cooperative software-hardware scheme for efficiently adding customization functionality---e.g, safety/security checking, profiling, dynamic code decompression, and dynamic optimization---to an application. In DISE, application customization functions (ACFs) are formulated as rules for macroexpanding certain instructions into parameterized instruction sequences. The processor executes the rules on the fetched instructions, feeding the executi ...

12 Spatial computation

Mihai Budiu, Girish Venkataramani, Tiberiu Chelcea, Seth Copen Goldstein

October 2004 ACM SIGARCH Computer Architecture News, ACM SIGPLAN Notices, ACM SIGOPS Operating Systems Review , Proceedings of the 11th international conference on Architectural support for programming languages and operating systems ASPLOS-XI, Volume 32, 39, 38 Issue 5, 11, 5

Publisher: ACM Press

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> Full text available: pdf(573.00 KB) terms, review

This paper describes a computer architecture, Spatial Computation (SC), which is based on the translation of high-level language programs directly into hardware structures. SC program implementations are completely distributed, with no centralized control. SC circuits are optimized for wires at the expense of computation units. In this paper we investigate a particular implementation of SC: ASH (Application-Specific Hardware). Under the assumption that computation is cheaper than co ...

Keywords: application-specific hardware, dataflow machine, low-power, spatial computation

13 Simulation and architecture evaluation: Using modern graphics architectures for general-purpose computing: a framework and analysis

Chris J. Thompson, Sahngyun Hahn, Mark Oskin

November 2002 Proceedings of the 35th annual ACM/IEEE international symposium on Microarchitecture MICRO 35

Publisher: IEEE Computer Society Press

Full text available: Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms Publisher Site

Recently, graphics hardware architectures have begun to emphasize versatility, offering rich new ways to programmatically reconfigure the graphics pipeline. In this paper, we

http://portal.acm.org/results.cfm?CFID=24150656&CFTOKEN=98113376&adv=1... 5/31/2007



































explore whether current graphics architectures can be applied to problems where general-purpose vector processors might traditionally be used. We develop a programming framework and apply it to a variety of problems, including matrix multiplication and 3-SAT. Comparing the speed of our graphics card implementations to stand ...

Chromium: a stream-processing framework for interactive rendering on clusters
Greg Humphreys, Mike Houston, Ren Ng, Randall Frank, Sean Ahern, Peter D. Kirchner,
James T. Klosowski



July 2002 ACM Transactions on Graphics (TOG), Proceedings of the 29th annual conference on Computer graphics and interactive techniques SIGGRAPH '02, Volume 21 Issue 3

Publisher: ACM Press

Full text available: pdf(27.83 MB)

Additional Information: full citation, abstract, references, citings, index terms

We describe Chromium, a system for manipulating streams of graphics API commands on clusters of workstations. Chromium's stream filters can be arranged to create sort-first and sort-last parallel graphics architectures that, in many cases, support the same applications while using only commodity graphics accelerators. In addition, these stream filters can be extended programmatically, allowing the user to customize the stream transformations performed by nodes in a cluster. Because our stream pr ...

Keywords: cluster rendering, parallel rendering, remote graphics, scalable rendering, stream processing, tiled displays, virtual graphics

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Ciglan, M.; Hluchy, L.;

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5. Replica Update Propagation for Grid Data Resources

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